



PTO/SB/21 (09-04)

Approved for use through 07/31/2006. OMB 0651-0031  
U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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**TRANSMITTAL  
FORM**

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

16

Application Number

10/686,882

Filing Date

October 15, 2003

First Named Inventor

Kenneth Mark Williams

Art Unit

2186

Examiner Name

Shane M. Thomas

Attorney Docket Number

PA2594US

**ENCLOSURES (Check all that apply)**

Fee Transmittal Form



Fee Attached



Amendment/Reply



After Final



Affidavits/declaration(s)



Extension of Time Request



Express Abandonment Request



Information Disclosure Statement



Certified Copy of Priority Document(s)

Reply to Missing Parts/  
Incomplete ApplicationReply to Missing Parts  
under 37 CFR 1.52 or 1.53

Drawing(s)



Licensing-related Papers



Petition

Petition to Convert to a  
Provisional Application

Power of Attorney, Revocation



Change of Correspondence Address



Terminal Disclaimer



Request for Refund



CD, Number of CD(s) \_\_\_\_\_

☐ Landscape Table on CD

After Allowance Communication to TC

Appeal Communication to Board  
of Appeals and InterferencesAppeal Communication to TC  
(Appeal Notice, Brief, Reply Brief)

Proprietary Information



Status Letter

Other Enclosure(s) (please identify  
below):

- 1) Confirmation postcard;
- 2) Duplicate of IDS;
- 3) PTO/SB/08 (4 pp. x2);
- 4) List of Co-Pending Applications;
- 5) Copies of 17 non-patent references.

Remarks

Total page number does not include postcard or check.

**SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT**

Firm Name

Carr &amp; Ferrell LLP

Cust. No. 22830

Signature

*Peter L. Holland*

Printed name

Peter L. Holland

Date

June 27, 2007

Reg. No.

57,113

**CERTIFICATE OF TRANSMISSION/MAILING**

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below:

Signature

*Peter L. Holland*

Typed or printed name

Peter L. Holland, Reg. No. 57,113

Date

June 27, 2007

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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JUN 29 2007

PTO/SB/17 (01-06)

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Fees pursuant to the Consolidated Appropriations Act, 2005 (H.R. 4818).

# FEE TRANSMITTAL

## For FY 2006

☐ Applicant claims small entity status. See 37 CFR 1.27

TOTAL AMOUNT OF PAYMENT (\$ ) 180.00

## Complete if Known

Application Number	10/686,882
Filing Date	October 15, 2003
First Named Inventor	Kenneth Mark Williams
Examiner Name	Shane M. Thomas
Art Unit	2186
Attorney Docket No.	PA2594US

## METHOD OF PAYMENT (check all that apply)

☒ Check ☐ Credit Card ☐ Money Order ☐ None ☐ Other (please identify): \_\_\_\_\_☒ Deposit Account Deposit Account Number: 06-0600 Deposit Account Name: Carr & Ferrell LLP

For the above-identified deposit account, the Director is hereby authorized to: (check all that apply)

☐ Charge fee(s) indicated below ☐ Charge fee(s) indicated below, except for the filing fee

☒ Charge any additional fee(s) or underpayments of fee(s) under 37 CFR 1.16 and 1.17 ☒ Credit any overpayments

WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.

## FEE CALCULATION (All the fees below are due upon filing or may be subject to a surcharge.)

## 1. BASIC FILING, SEARCH, AND EXAMINATION FEES

Application Type	FILING FEES		SEARCH FEES		EXAMINATION FEES		Fees Paid (\$)
	Fee (\$)	Small Entity Fee (\$)	Fee (\$)	Small Entity Fee (\$)	Fee (\$)	Small Entity Fee (\$)	
Utility	300	150	500	250	200	100	
Design	200	100	100	50	130	65	
Plant	200	100	300	150	160	80	
Reissue	300	150	500	250	600	300	
Provisional	200	100	0	0	0	0	

## 2. EXCESS CLAIM FEES

Fee Description	Fee (\$)	Small Entity Fee (\$)
Each claim over 20 (including Reissues)	50	25
Each independent claim over 3 (including Reissues)	200	100
Multiple dependent claims	360	180

**Total Claims**      **Extra Claims**      **Fee (\$)**      **Fee Paid (\$)**      **Multiple Dependent Claims**

- 20 or HP = \_\_\_\_\_ x \_\_\_\_\_ = \_\_\_\_\_

HP = highest number of total claims paid for, if greater than 20.

**Indep. Claims**      **Extra Claims**      **Fee (\$)**      **Fee Paid (\$)**

- 3 or HP = \_\_\_\_\_ x \_\_\_\_\_ = \_\_\_\_\_

HP = highest number of independent claims paid for, if greater than 3.

## 3. APPLICATION SIZE FEE

If the specification and drawings exceed 100 sheets of paper (excluding electronically filed sequence or computer listings under 37 CFR 1.52(e)), the application size fee due is \$250 (\$125 for small entity) for each additional 50 sheets or fraction thereof. See 35 U.S.C. 41(a)(1)(G) and 37 CFR 1.16(s).

Total Sheets	Extra Sheets	Number of each additional 50 or fraction thereof	Fee (\$)	Fee Paid (\$)
_____	_____	_____	_____	_____

- 100 = \_\_\_\_\_ / 50 = \_\_\_\_\_ (round up to a whole number) x \_\_\_\_\_ = \_\_\_\_\_

## 4. OTHER FEE(S)

Non-English Specification, \$130 fee (no small entity discount)      **Fees Paid (\$)**

Other (e.g., late filing surcharge): Information Disclosure Statement Filing Fee      180

## SUBMITTED BY

Signature	<i>Peter L. Holland</i>	Registration No. (Attorney/Agent) 57,113	Telephone 650.812.3400
Name (Print/Type)	Peter L. Holland	Date June 27, 2007	

This collection of information is required by 37 CFR 1.136. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 30 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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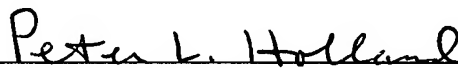
IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS: Kenneth Mark Williams et al.  
APPLICATION NO.: 10/686,882  
FILED: October 15, 2003  
TITLE: Instruction Set for Efficient Bit Stream and Byte Stream I/O  
EXAMINER: Shane M. Thomas  
GROUP ART UNIT: 2186  
ATTY.DKT.NO.: PA2594US

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CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on June 27, 2007.



Peter L. Holland, Reg. No. 57,113

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P.O. BOX 1450  
ALEXANDRIA, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT  
Under 37 C.F.R. §§ 1.56, and 1.97-1.98

SIR:

Pursuant to the provisions of 37 C.F.R. §§ 1.56 and 1.97-98 of the Rules of Practice in Patent Cases, enclosed herewith is form PTO-SB-08, listing several references, and a List of Co-Pending Applications.

The Examiner is requested to make these references of official record in the application. The references cited may be material to examination of the application and are submitted in compliance with Applicants' duty of disclosure as defined by 37 C.F.R. § 1.56. No representation is made or intended as to the completeness of this list, nor is the inclusion of any reference on this list an admission that it is prior art or pertinent to this application.

Applicants have enclosed the applicable fee of \$180.00 with this submission, in accordance to C.F.R. § 1.17(p). If more fees are due, however, the Commissioner is hereby authorized to charge any necessary fees to Account Number 06-0600. A copy of this statement is submitted in duplicate for this purpose.

Respectfully submitted,  
Kenneth Mark Williams et al.

June 27, 2007

By: \_\_\_\_\_

*Peter L. Holland*

Peter L. Holland, Reg. No. 57,113  
**Carr & Ferrell LLP**  
2200 Geng Road  
Palo Alto, CA 94303  
TEL: (650) 812-3400  
FAX: (650) 812-3444



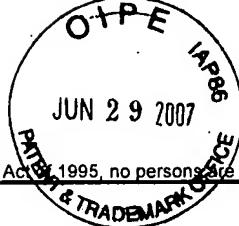
**IN THE  
UNITED STATES PATENT AND TRADEMARK OFFICE**

APPLICANT(S): Kenneth Mark Williams et al.  
APPLICATION NO.: 10/686,882  
FILED: October 15, 2003  
TITLE: Instruction Set for Efficient Bit Stream and Byte Stream I/O  
EXAMINER: Shane M. Thomas  
GROUP ART UNIT: 2186  
ATTY.DKT.NO.: PA2594US

COMMISSIONER FOR PATENTS  
P.O. BOX 1450  
ALEXANDRIA, VA 22313-1450

**List of Co-Pending Patents or Patent Applications That May Be Directed  
Towards Similar Subject Matter**

Examiner's Initials	Application Number	First-Named Inventor	Title	Filing Date
	10/630,542	Kenneth Williams	Defining Instruction Extensions in a Standard Programming Language	07-29-2003
	10/404,706	Scott D. Johnson	Extension Adapter	03-31-2003
	10/394,824	Charle' Rupp	Transitive Processing Unit for Performing Complex Operations	03-21-2003
	10/750,714	Ricardo Gonzalez	Systems and Methods for Software Extensible Multi-Processing	12-31-2003
	10/815,453	Ricardo Gonzalez	Configuring a Multi-Processor System	03-31-2004
	11/021,247	Ricardo Gonzalez	Systems and Methods for Selecting Input/Output Configuration in an Integrated Circuit	12-21-2004
	11/129,146	Ricardo Gonzalez	Long Instruction Word Processing with Instruction Extensions	05-12-2005
	11/099,280	Jeffrey M. Arnold	Video Processing System with Reconfigurable Instructions	04-04-2005
	11/204,555	Jeffrey M. Arnold	Programmable Logic Configuration for Instruction Extensions	08-15-2005
Examiner's Signature:				Date



PTO/SB/08A (09-06)

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U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

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Substitute for form 1449/PTO

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet 1 of 4

**Complete if Known**

Application Number	10/686,882
Filing Date	October 15, 2003
First Named Inventor	Kenneth Mark Williams
Art Unit	2186
Examiner Name	Shane M. Thomas
Attorney Docket Number	PA2594US

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
		US- 6,237,079 B1	05-22-2001	Stoney	
		US-			
		US- 5,847,578	12-08-1998	Noakes et al.	
		US- 6,415,424 B1	07-02-2002	Arimilli et al.	
		US- 6,795,900 B1	09-21-2004	Miller et al.	
		US- 5,682,493	10-28-1997	Yung et al.	
		US- 6,219,628 B1	04-17-2001	Kodosky et al.	
		US- 5,652,875	07-29-1997	Taylor	
		US- 5,696,956	12-09-1997	Razdan et al.	
		US- 5,784,636	07-21-1998	Rupp	
		US- 5,819,064	10-06-1998	Razdan et al.	
		US- 6,732,354 B2	05-04-2004	Ebeling et al.	
		US- 2003/0097546 A1	05-22-2003	Taylor	
		US- 2003/0108119 A1	06-12-2003	Mohebbi et al.	
		US- 6,721,884 B1	04-13-2004	De Oliveira Kastrup Pereira et al.	
		US- 6,426,648 B1	07-30-2002	Rupp	
		US- 7,062,520 B2	06-13-2006	Rupp	
		US- 6,857,110 B1	02-15-2005	Rupp et al.	
		US- 6,744,274 B1	06-01-2004	Arnold et al.	

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> *Number <sup>4</sup> *Kind Code <sup>5</sup> (if known)				
		TW 480818	03-21-2002	Adaptive Silicon, Inc.		
		TW 509798	11-11-2002	Adaptive Silicon, Inc.		
		TW 478252	03-01-2002	Adaptive Silicon, Inc.		
		EP 1 443 417	08-04-2004	STMicroelectronics		

Examiner Signature	Date Considered
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> See Kinds Codes of USPTO Patent Documents at [www.uspto.gov](http://www.uspto.gov) or MPEP 901.04. <sup>3</sup> Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). <sup>4</sup> For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup> Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup> Applicant is to place a check mark here if English language Translation is attached.

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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Sheet

2

of

4

**Complete if Known**

Application Number	10/686,882
Filing Date	October 15, 2003
First Named Inventor	Kenneth Mark Williams
Art Unit	2186
Examiner Name	Shane M. Thomas
Attorney Docket Number	PA2594US

U. S. PATENT DOCUMENTS					
Examiner Initials*	Cite No. <sup>1</sup>	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code <sup>2</sup> (if known)			
		US- 6,874,110 B1	03-29-2005	Camarota	
		US- 6,292,388 B1	09-18-2001	Camarota	
		US- 6,418,045 B2	07-09-2002	Camarota	
		US- 7,000,211 B2	02-14-2006	Arnold	
		US- 6,954,845	10-11-2005	Arnold et al.	
		US- 6,698,015 B1	02-24-2004	Moberg et al.	
		US- 6,799,236 B1	09-28-2004	Dice et al.	
		US- 5,822,588	10-13-1998	Sterling et al.	
		US- 6,817,013 B2	11-09-2004	Tabata et al.	
		US- 6,968,544 B1	11-22-2005	Schneider	
		US- 7,178,062 B1	02-13-2007	Dice	
		US- 4,766,569	08-23-1998	Turner et al.	
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FOREIGN PATENT DOCUMENTS						
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		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)				

Examiner Signature	Date Considered
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Substitute for form 1449/PTO  <h2 style="text-align: center;">INFORMATION DISCLOSURE STATEMENT BY APPLICANT</h2> <p style="text-align: center;">(Use as many sheets as necessary)</p>				<b>Complete if Known</b>	
				Application Number	10/686,882
				Filing Date	October 15, 2003
				First Named Inventor	Kenneth Mark Williams
				Art-Unit	2186
				Examiner Name	Shane M. Thomas
				Attorney Docket Number	PA2594US
Sheet	3	of	4		

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		GARLAN, DAVID and MARY SHAW, "An Introduction to Software Architecture," January 1994, CMU-CS-94-166, School of Computer Science, Carnegie Mellon University, Pittsburgh, PA.	
		INTEL and HEWLETT-PACKARD, "IA-64 Application Instruction Set Architecture Guide," Revision 1.0, 1999, pp. C-1 through C.	
		DEHON, ANDRE, "Transit Note #118 Notes on Coupling Processors with Reconfigurable Logic," M.I.T. Transit Project, Last Updated March 21, 1995.	
		HWANG, KAI, "Advanced Computer Architecture: Parallelism, Scalability, Programmability," 1993, McGraw Hill, pp. 182-183.	
		YE, Z.A. et al., "CHIMAERA: a high-performance architecture with a tightly-coupled reconfigurable functional unit," June 10-14, 2000, Proc. of the 27th International Symposium on Computer Architecture, pp. 225-235.	
		LEE, K.C., "A Virtual Bus Architecture for Dynamic Parallel Processing," February 1993, IEEE Transactions on Parallel and Distributed Systems, Vol. 4, No. 2, pp. 121-130.	
		GOLDBLATT, KIM, "The Low-Cost, Efficient Serial Configuration of Spartan FPGAs," November 13, 1998, XAPP098 (Version 1.0), XILINX.	
		SCOTT, STEVEN L. and GREGORY M. THORSON, "The Cray T3E Network: Adaptive Routing in a High Performance 3D Torus," August 15-16, 1996, HOT Interconnects IV, Stanford University.	
		GOLESTANI, S. JAMALODDIN, "A Stop-and-Go Queuing Framework for Congestion Management," 1990, Proc. of the ACM Symposium on Communications Architectures & Protocols, ACM Press, New York, NY, pp. 8-18.	
		DALLY, WILLIAM J. and CHARLES L. SEITZ, "Deadlock Free Message Routing in Multiprocessor Interconnection Networks," May 10, 1985, Computer Science Department, California Institute of Technology.	

Examiner Signature	Date Considered
--------------------	-----------------

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<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached. This collection of information is required by 37 CFR 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Substitute for form 1449/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  <i>(Use as many sheets as necessary)</i>		<b>Complete if Known</b>	
		Application Number	10/686,882
		Filing Date	October 15, 2003
		First Named Inventor	Kenneth Mark Williams
		Art Unit	2186
		Examiner Name	Shane M. Thomas
Sheet 4	of 4	Attorney Docket Number	PA2594US

NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		MIPS Technologies, Inc., "MIPS Extension for Digital Media with 3D," 1996, pp. 1-26.	
		BORGATTI, MICHELE et al., "A Reconfigurable System featuring Dynamically Extensible Embedded Microprocessor, FPGA and Customisable I/O," 2002, IEEE Custom Integrated Circuits Conference, pp. 1-4.	
		RAZDAN, RAHUL and MICHAEL D. SMITH, "A High-Performance Microarchitecture with Hardware-Programmable Functional Units," November 1994; Proc. of MICRO-27, pp. 1-9.	

Examiner Signature	Date Considered
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup> Applicant's unique citation designation number (optional). <sup>2</sup> Applicant is to place a check mark here if English language Translation is attached.

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